

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S81	13	variable adj writ\$3 and "data mask"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/05 13:30
S38	218	support\$4 near5 (different various) near5 type near5 memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/05 08:27
S42	279	support\$4 near5 (different various multiple plural plurality) near5 type near5 memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/05 08:27
S45	1	S42 and terminat\$3 near3 resist\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/05 09:00
S43	7	S42 and bank adj (busy cycle)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/05 08:46
S39	10	S38 and bank near5 (busy cycle)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/05 08:27
S14	2	nwram	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/31 09:31
S30	241	mount-type	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/31 11:22
S92	69	mode adj register same "auto refresh" same (precharge pre-charge)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/05 15:24
S19	107	memory near3 (controller control) same bank near3 busy	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/31 10:02

S35	56	memory near (controller control) same "write mask"	US-PGPUB; USPAT; USOCR	OR	ON	2005/08/31 12:36
S32	1	memory near (controller control) and bank near3 cycle and "write mask" and "initial sequence"	US-PGPUB; USPAT; USOCR	OR	ON	2005/08/31 12:19
S34	46	memory near (controller control) and "write mask" and bank near3 (cycle busy)	US-PGPUB; USPAT; USOCR	OR	ON	2005/08/31 12:23
S33	378	memory near (controller control) and "write mask"	US-PGPUB; USPAT; USOCR	OR	ON	2005/08/31 12:19
S65	2	memory adj (control\$3) same sequenc\$3 near3 (initial initialization initializing) same "ddr sdram"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/05 09:47
S63	2	memory adj (control\$3) same sequenc\$3 near3 (initial initialization initializing) and fcram	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/05 09:46
S64	28	memory adj (control\$3) same sequenc\$3 near3 (initial initialization initializing) and "ddr sdram"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/05 14:53
S80	160	memory adj (control\$3) same sequenc\$3 near3 (initial initialization initializing)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/05 13:30
S91	22	memory adj (control\$3) and "ddr sdram" and "mode register" and extended adj mode	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/05 15:17
S90	50	memory adj (control\$3) and "ddr sdram" and "mode register set"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/05 15:15
S89	3	memory adj (control\$3) and "ddr sdram" and "extension mode register"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/05 14:56

S57	1	mask same fcram same ("ddr sdram")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/05 09:13
S56	23	mask and fcram and ("ddr sdram") and variable	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/05 09:13
S55	52	mask and fcram and ("ddr sdram") and memory adj (control\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/05 09:44
S54	54	mask and fcram and ("ddr sdram") and (switch\$3 multiplex\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/05 09:11
S53	56	mask and fcram and ("ddr sdram")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/05 09:12
S52	83	mask and fcram	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/05 09:10
S15	164	fcram same (ddr ddr-sdram sdram)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/31 09:34
S16	130	fcram near5 (ddr ddr-sdram sdram)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/31 09:35
S17	111	fcram near3 (ddr ddr-sdram sdram)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/31 09:35
S18	84	fcram near (ddr ddr-sdram sdram)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/31 09:35

S11	1	fcram and nwrnram and ddr-sdram	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/31 09:31
S12	2	fcram and nwrnram	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/31 09:31
S36	3	fcram and "variable write"	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/01 05:24
S13	296	fcram	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/31 09:34
S93	4	extended adj mode adj register same "auto refresh" same (precharge pre-charge)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/05 15:33
S94	2	emrs same mrs same precharge same auto-refresh	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/05 15:33
S22	55	different near5 type near5 memory same memory adj (controller control) same timing	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/31 11:01
S8	7	different near5 type near5 memory same memory adj (controller control) same (adjust)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/31 09:07
S7	456	different near5 type near5 memory same memory adj (controller control)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/31 10:00
S23	2	different near5 type near5 memory same bank near3 busy	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/31 10:21

S6	8142	different near5 type near5 memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/31 09:03
S73	24	bank near5 busy near5 register	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/05 12:12
S76	4	bank near5 (cycle busy) near5 register near5 counter	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/05 12:38
S74	263	bank near5 (cycle busy) near5 register	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/05 12:37
S75	136	bank near3 (cycle busy) near3 register	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/05 12:37
S9	266	adjustable with memory with controller	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/31 09:56
S4	2	accomodat\$3 near3 (various different) near3 type near5 memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/31 09:02
S28	715	(programming programmable) near memory near (controller control)	US-PGPUB; USPAT; USOCR	OR	ON	2005/08/31 10:30
S29	563	(programmable) near memory near (controller control)	US-PGPUB; USPAT; USOCR	OR	ON	2005/08/31 12:16
S48	1	(programmable and memory and controller).ti. and terminat\$3 near3 resist\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/05 09:03
S47	807	(programmable and memory and controller).ti.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/05 09:02

S85	1	(different various multiple plural plurality several) near3 type near3 memory same "address interface"	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/05 14:13
S87	70	(different various multiple plural plurality several) near3 type near3 memory and "address interface" and mask\$3	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/05 14:14
S86	142	(different various multiple plural plurality several) near3 type near3 memory and "address interface"	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/05 14:13
S21	1771	(711/170).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/31 09:57
S20	1430	(711/167).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/31 09:57
S78	642	(365/233.5).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/05 13:23
S77	3154	(365/233).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/05 13:20
L1	3530	(365/230.03).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/06 10:41
S25	2	("6516396" "6553472").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2005/08/31 10:24
S59	2	("6219747" "6286077").PN. OR ("6483772").URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/05 09:25
S71	13	("5857095" "5886948" "5917761" "6073223" "6137734" "6292903" "6370067" "6389522" "6397312").PN. OR ("6553472").URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/05 11:27
S82	8	("5412788" "5440713" "5548793" "6026464").PN. OR ("6115768").URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/05 14:03

S44	7	("5176525" "5257166" "5287455" "5495435" "5671149" "5797031" "5982655").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/05 08:39
S10 0	12	("20010050858" "20020024834" "5164916" "5802395" "6049476" "6142830" "6172895" "6308232" "6311310" "6480409" "6505331" "6567337").PN. OR ("6930904").URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/06 07:11
S51	4	"write mask" and fcram	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/05 09:10
S50	172	"write mask" and ddr	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/05 09:06
S49	1233	"write mask"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/05 09:06
S67	20	"memory controller" same "bank busy"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/05 11:21
S69	6	"memory controller" and "bank busy" same counter	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/05 11:23
S68	62	"memory controller" and "bank busy"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/05 11:21
S70	38	"bank busy" same counter	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/05 12:12
S72	11	"bank busy" near5 register	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/05 12:37

S41	3	"bank busy" and "write mask"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/05 08:22
S40	292	"bank busy"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/05 08:22
S83	84	"address interface" same memory adj control\$3	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/05 14:11